

CLAIMS:

1. An electronic circuit comprising conversion means (CNV) for converting an input voltage (U_i) into an output voltage (U_0), comprising at least a first energy storage means (C1) and a second energy storage means (C2) and switching means (Sw1, Sw2) for periodically coupling said energy storage means (C1, C2) to one another under the control of a clock signal so as to store energy in the energy storage means (C1, C2) and transferring at least a portion of the stored energies between the energy storage means (C1, C2), characterized in that the clock signal is kept in a holding state during a holding period (R_T) during operation, which holding state is equal to the state of the clock signal immediately before the holding state.
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2. An electronic circuit as claimed in claim 1, characterized in that the switching means and the energy storage means are implemented with the use of at least one charge pump (CHGPMP1 – CHGPMP4).
- 15 3. A medium for storage/reading of user information, comprising an integrated circuit (IC) that comprises an electronic circuit as defined in claim 1 or 2.
- 20 4. A medium as claimed in claim 3, characterized in that the integrated circuit (IC) comprises a photosensitive sensor (SNS) for providing the input voltage (U_i) when the sensor (SNS) receives a substantial quantity of light.
- 25 5. A medium as claimed in claim 4, characterized in that the integrated circuit (IC) furthermore comprises memory means (MM) which are provided with a supply voltage through utilization of the output voltage (U_0).
6. A medium as claimed in claim 5, characterized in that the IC in addition comprises a microprocessor (uP) and a further photosensitive sensor (SNS_F) for providing additional information to the microprocessor (uP), which microprocessor (uP) processes the

additional information, and which microprocessor (uP) is coupled to the memory means (MM) for storing the processed additional information.

7. A medium as claimed in claim 5, characterized in that the integrated circuit (IC) further comprises a microprocessor (uP) and a further photosensitive sensor (SNS_F) for providing additional information to the memory means (MM) for the storage of the additional information, and in that the microprocessor (uP) is coupled to the memory means (MM) for processing the additional information after reading of the additional information from the memory means (MM).

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8. A medium as claimed in claim 6 or 7, characterized in that the length of the holding period (R_T) corresponds by approximation to that of a time period during which the photosensitive sensor (SNS) does not receive a substantial quantity of light.

15 9. A medium as claimed in claim 8, characterized in that the microprocessor (uP) is idle during the holding period (R_T), and in that the microprocessor (uP) is provided with a supply voltage from a standby circuit (SB) during the holding period (R_T).

10. A medium as claimed in claim 3, 4, 5, 6, 7, 8, or 9, characterized in that the 20 medium is an optical disc which has a side for storing and reading of the user information, while the integrated circuit (IC) is fastened to said side of the optical disc in a region not reserved for storing and reading of the user information.

25 11. A medium as claimed in claim 3, 4, 5, 6, 7, 8, or 9, characterized in that the medium is an optical disc which has a first side for storing and reading of the user information, while the integrated circuit (IC) is fastened to a second side of the optical disc.

12. A recording/playback device for storage/reading of information onto/from a medium as defined in claim 3, 4, 5, 6, 7, 8, 9, 10, or 11.